Subclass	ISSUE CLASSIFICATION
Class	ISSUE CLAS

FILED UNDER 95 U.S.C. 9'

PATENT NUMBER
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U.S. UTILITY Patent Application

SCANNED.

PATENT DATE

ART UNIT EXAMINER SUBCLASS CONT/PRIOR CLASS APPLICATION NO. 1734 MUSSED 156 09/719422 D F 242

Yoshihisa Furuta Yoshinori Watanabe

Semiconductor chip resin-sealing method and adhesive tape for pasting lead frames or the like

PTO-2040 12/99

ISSUING CLASSIFICATION							
ORIGINAL			CROSS R	EFERENCE	E(S),		
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TERMINAL	DRAWINGS	CLAIMS ALLOWED
DISCLAIMER	Sheets Drwg. Figs. Drwg. Print Fig.	Total Claims Print Claim for O.G.
The term of this patent subsequent to (date) has been disclaimed.	(Assistant Examiner) (Date)	NOTICE OF ALLOWANCE MAILED
The term of this patient shall not extend beyond the expiration data of U.S Patient. No.		ISSUE FEE
	(Primary Exeminer) (Date)	Amount Due Date Paid
The terminal months of this patent have been disclaimed:	(Legal Instrumenta Examinar) (Cate)	ISSUE BATCH NUMBER
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